



IP-Maker to release NVMe PCIe Gen 3 reference design

For custom design of high performance and low power storage devices

July 11, 2016, Aix en Provence, France: IP-Maker, the French startup specialized in high performance storage technologies, released its new NVMe Express (NVMe) reference design, using a PCIe Gen3 interface. It is based on the low latency hardware NVMe engine designed by IP-Maker.

“The IP-Maker team is excited to announce the availability of this new reference design” said Mickael Guyard, Marketing Director at IP-Maker. “The combination of the high speed PCIe Gen 3 interface with the low latency and high bandwidth features of our NVMe technology will accelerate cloud and big data applications through high performance storage devices”.

The IP-Maker PCIe Gen 3 NVMe reference design is based on the VC709 Xilinx evaluation kit. The NVMe IP is integrated with the advanced Virtex-7 PCIe Gen3 hard IP and a soft DDR3 controller. It uses a x4 lanes configuration. This UNH-IOL NVMe compliant reference design demonstrates the end-to-end low latency NVMe management. This reference design is an ideal starting point to design storage solutions such as flash-based SSD, NVMe HBA, NVRAM SSD, NG-NVM SSD or even custom all flash array.

The IP-Maker PCIe Gen 3 NVMe reference design is available now and will be showcased at the Flash Memory Summit event.

About IP-Maker

IP-Maker is a leader in Intellectual Properties (IP) for high performance storage applications. IP-Maker’s NVMe Express (NVMe) technology provides a unique hardware accelerated solution that leverages the PCIe SSD performances, including ultra-low latency and high throughput. IP-Maker is a contributor to the NVMe specification. The ASIC and FPGA IP portfolio includes NVMe, Universal NandFlash Controller and ECC IP cores. The combination of the IP-Maker technology and its associate services dramatically cuts time-to-market.

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